

ID	R <sub>Ds</sub> (ON)(Typ)	VDSS
20A	0.21Ω	500V

#### Applications:

- Switch Mode Power Supply(SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)

#### Features:

- Fast switching speed
- 100% avalanche tested
- Improved dv/dt capability

# $\mathbf{r}_{\mathbf{r}}$

#### **Ordering Information**

Part Number	Package	Marking	Packing	Qty.
RS20N50F	T0-220F	RS20N50F	Tube	50 PCS

#### Absolute Maximun Ratings Tc= $25^{\circ}$ C unless otherwise specified

Symbol	Parameter	RS20N50F	Units
VDSS	Drain-to-Source Voltage	500	V
ID	Continuous Drain Current TC=25℃	20	۸
IDM	Pulsed Drain Current (Note*1)	80	A
PD	Power Dissipation	130	W
VGS	Gate- to- Source Voltage	±30	V
EAS	Single Pulse Avalanche Engergy L = 10mH, VDD = 50V, RG = 25 $\Omega$	1400	mJ
	Maximum Temperature for Soldering		
TL TPKG	Leads at 0.063in(1.6mm)from Case for 10 seconds Package Body for 10 seconds	300 260	°C
TJ and TSTG	Operating Junction and Storage Temperature Range	-55 to 150	

\* Drain Current Limited by Maximum Junction Temperature

Caution: Stresses greater than those listed in the "Absolute Maximum Ratings" Table may cause permanent damage to the device.



## **Thermal Resistance**

Symbol	Parameter	RS20N50F	Units	Test Conditions
RØJC	Junction-to-Case	0.95	°C/W	Drain lead soldered to water cooled heatsink, PD adjusted for a peak junction temperature of + 1 5 0 $^\circ$ C
RθJA	Junction-to- Ambient	48.78		1 cubic foot chamber,free air.

# **OFF Characteristics** TJ= $25^{\circ}$ C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
BVDSS	Drain- to- source Breakdown Voltage	500			V	VGS=0V,ID=250μ Α
IDSS	Drain- to- Source Leakage Current			1	μΑ	VDS=500V,VGS= 0V
	Gate- to- Source Forward Leakage			100	~^^	VGS=30V ,VDS=0 V
IGSS	Gate- to- Source Reverse Leakage			-100	- nA	VGS=-30V ,VDS= 0V

# **ON Characteristics** TJ=25°C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
RDS(on)	Static Drain- to- Source On- Resistance(Note*2)		0.21	0.27	Ω	VGS=10V,ID=10A
VGS(TH)	Gate Threshold Voltage	2		4	V	VGS=VDS,ID=25 0μA

### **Resistive Switching Characteristics** Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
td(ON)	Turn- on Delay Time		52		- nS	VDS=250V ID=20A RG=25Ω
trise	Rise Time		69			
td(OFF)	Turn- OFF Delay Time		88			
tfall	Fall Time		17			



Symbol	Parameter	Min.	Тур.	Max.	Units	<b>Test Conditions</b>
Ciss	Input Capacitance		2050			VGS=0V
Coss	Output Capacitance		274		pF	VDS=25V f=1.0MHz
Crss	Reverse Transfer Capacitance		16.5			
Qg	Total Gate Charge		70			VDS=400V
Qgs	Gate- to- Source Charge		15.5		nC	ID=20A VGS=10V
Qgd	Gate-to-Drain(" Miller") Charge		27.5			

#### **Dynamic Characteristics** Essentially independent of operating temperature

#### **Source- Drain Diode Characteristics**

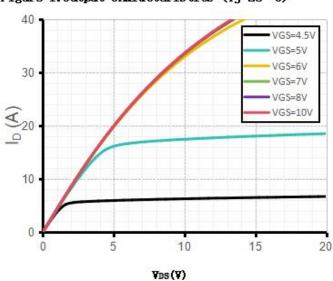
Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
IS	Continuous Source Current			20	А	Integral pn- diode
ISM	Maximum Pulsed Current			80	А	in MOSFET
VSD	Diode Forward Voltage			1.2	V	IS=10A,VGS=0V
trr	Reverse Recovery Time		550		nS	VGS=0V
Qrr	Reverse Recovery Charge		6.08		μC	IS=20A,di/dt=100 A/µs

#### Notes:

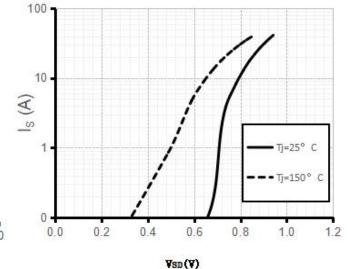
- \* 1. Repetitive rating, pulse width limited by maximum junction temperature.
- \* 2. Pulse Test: Pulse width  $\leq$  300µs, Duty Cycle  $\leq$  1%

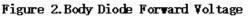


### **Typical Feature Curve**

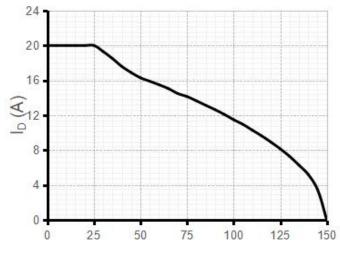


#### Figure 1.Output Characteristics (Tj=25°C)













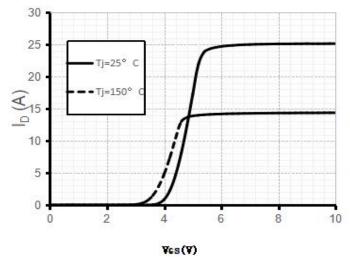


Figure 4. BVDSS Variation vs. Temperature

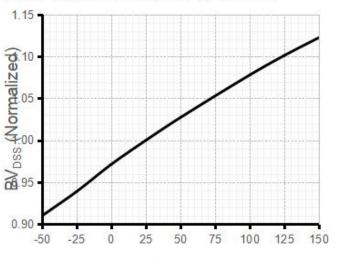
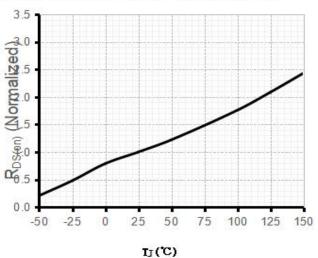




Figure 6. On-Resistance vs. Temperature

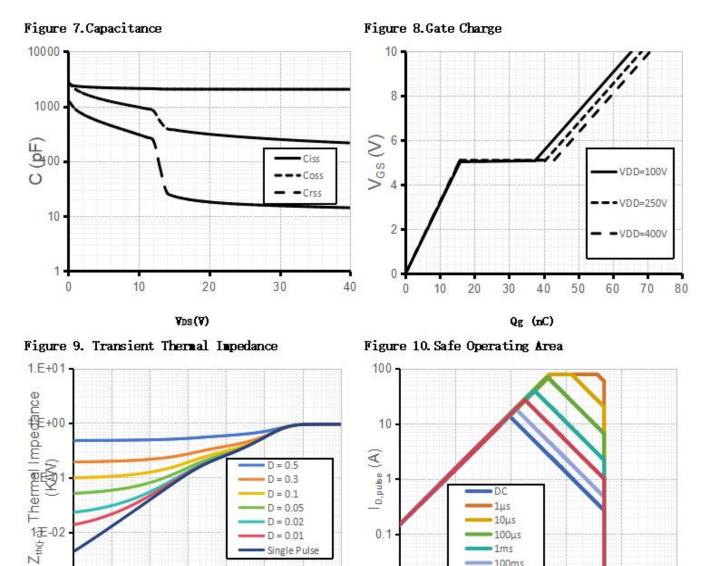


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# **Typical Feature Curve**



0.1

0.01

0.1

1

D = 0.05

D = 0.02

D = 0.01

1.E-06 1.E-05 1.E-04 1.E-03 1.E-02 1.E-011.E+001.E+01

tPW, Pulse time (sec)

Single Pulse

1µs

• 10µs

• 100µs

100ms 10ms

10

VDS(V)

100

1000

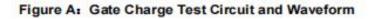
10000

1ms

1.E-03



#### **Test Circuits and Waveforms**



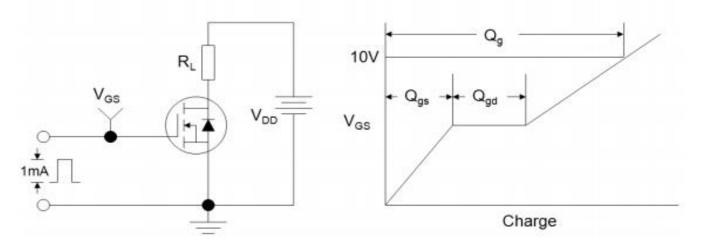


Figure B: Resistive Switching Test Circuit and Waveform

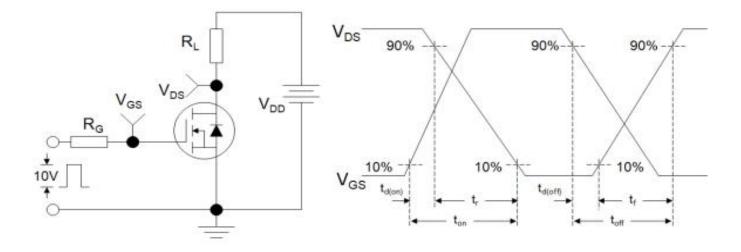
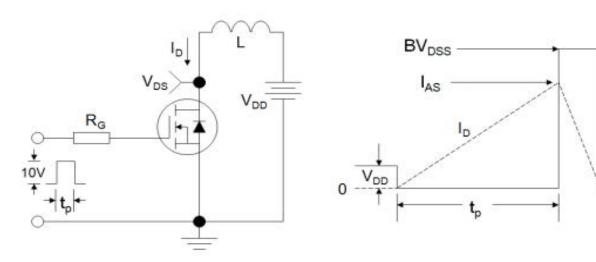


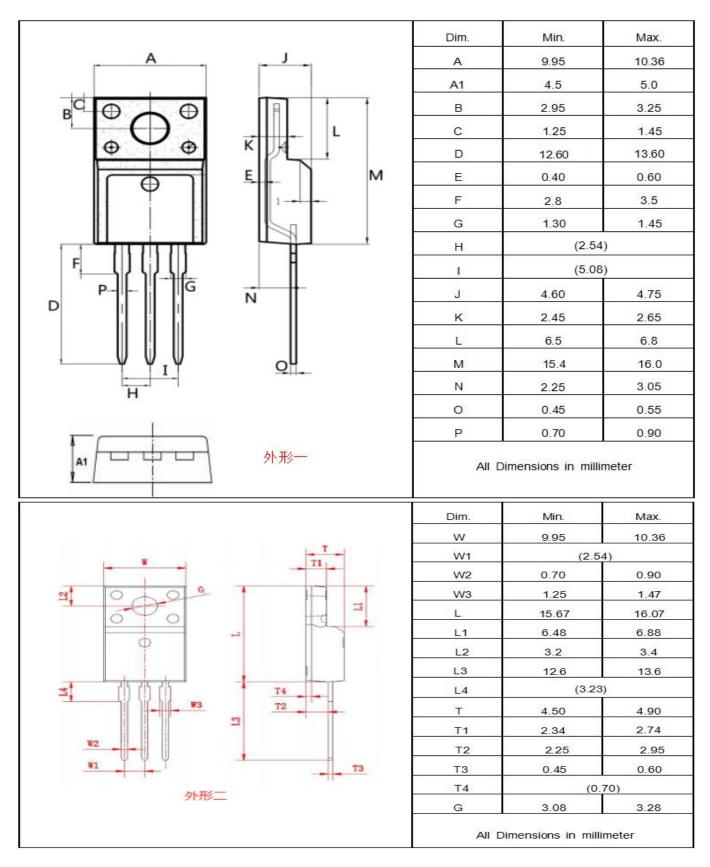
Figure Ct Unclamped Inductive Switching Test Circuit and Waveform



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# Package outline drawing(TO-220F Unit: mm)





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