

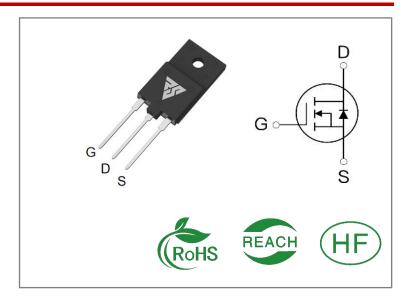
ID	R <sub>DS</sub> (ON)(Typ)	VDSS
3A	5.4Ω	1500V

## **Applications:**

- Switch Mode Power Supply(SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)

#### **Features:**

- Fast switching speed
- 100% avalanche tested
- Improved dv/dt capability



## **Ordering Information**

Part Number	Package	Marking	Packing	Qty.
RS3N150PF	TO-3PF	RS3N150PF	Tube	30 PCS

## Absolute Maximun Ratings Tc= 25°C unless otherwise specified

Symbol	Parameter	RS3N150PF	Units
VDSS	Drain-to-Source Voltage	1500	V
ID	Continuous Drain Current TC=25℃	3	Δ
IDM	Pulsed Drain Current (Note*1)	12	Α
PD	Power Dissipation	90	W
VGS	Gate- to- Source Voltage	±30	V
EAS	Single Pulse Avalanche Engergy L = 30mH, VDD = 50V, RG = 25 Ω	500	mJ
	Maximum Temperature for Soldering		
TL TPKG	Leads at 0.063in(1.6mm)from Case for 10 seconds Package Body for 10 seconds	300 260	$^{\circ}\!\mathrm{C}$
TJ and TSTG	Operating Junction and Storage Temperature Range	-55 to 150	

<sup>\*</sup> Drain Current Limited by Maximum Junction Temperature

Caution: Stresses greater than those listed in the "Absolute Maximum Ratings" Table may cause permanent damage to the device.



## **Thermal Resistance**

Symbol	Parameter	RS3N150PF	Units	Test Conditions
RθJC	Junction-to-Case	1.38	°C/W	Drain lead soldered to water cooled heatsink, PD adjusted for a peak junction temperature of + 1 5 0 $^{\circ}\mathrm{C}$
RθJA	Junction-to- Ambient	50		1 cubic foot chamber,free air.

## **OFF Characteristics** TJ= 25℃ unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
BVDSS	Drain- to- source Breakdown Voltage	1500			V	VGS=0V,ID=250μ A
IDSS	Drain- to- Source Leakage Current			1	μΑ	VDS=1500V,VGS =0V
IGSS	Gate- to- Source Forward Leakage		1	100	<b></b> Λ	VGS=30V ,VDS=0 V
1033	Gate- to- Source Reverse Leakage			-100	nA	VGS=-30V ,VDS= 0V

# ON Characteristics TJ=25°C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
RDS(on)	Static Drain- to- Source On- Resistance(Note*2)		5.4	6.4	Ω	VGS=10V,ID=2A
VGS(TH	Gate Threshold Voltage	2.5		4.5	V	VGS=VDS,ID=25 0μA

# Resistive Switching Characteristics Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
td(ON)	Turn- on Delay Time		25			
trise	Rise Time		48			VDS=750V
td(OFF)	Turn- OFF Delay Time		57		nS	ID=3A RG=4.7Ω
tfall	Fall Time		52			



**Dynamic Characteristics** Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Ciss	Input Capacitance		1600			VGS=0V
Coss	Output Capacitance		100		рF	VDS=25V
Crss	Reverse Transfer Capacitance		33			f=1.0MHz
Qg	Total Gate Charge		36			VDS=750V
Qgs	Gate- to- Source Charge		9.5		nC	ID=3A
Qgd	Gate-to-Drain(" Miller") Charge		12			VGS=10V

## **Source-Drain Diode Characteristics**

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
IS	Continuous Source Current			3	Α	Integral pn- diode
ISM	Maximum Pulsed Current			12	Α	in MOSFET
VSD	Diode Forward Voltage			1.5	V	IS=3A,VGS=0V
trr	Reverse Recovery Time		255		nS	VGS=0V
Qrr	Reverse Recovery Charge		1.1		μС	IS=3A,di/dt=100A /μs

## Notes:

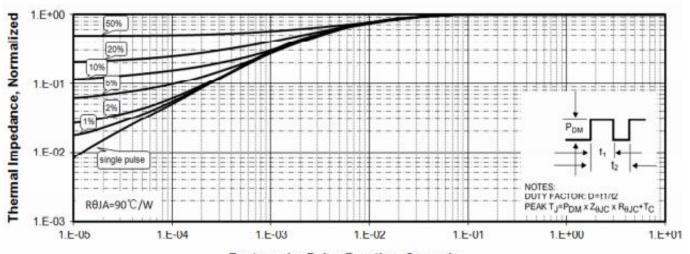
<sup>\* 1.</sup> Repetitive rating, pulse width limited by maximum junction temperature.

<sup>\* 2.</sup> Pulse Test: Pulse width ≤ 380μs, Duty Cycle ≤ 2%



#### **Typical Feature Curve**

Figure 1. Maximum Transient Thermal Impedance



Rectangular Pulse Duration, Seconds

Figure 2. Max. Power Dissipation vs Case Temperature 100 Pd, Power Dissipation, Watts 80 60 TO-SPE 40 20 0 50 75 100 125 25 150 Tc, Case Temperature, \*C

Figure 4. Output Characteristics

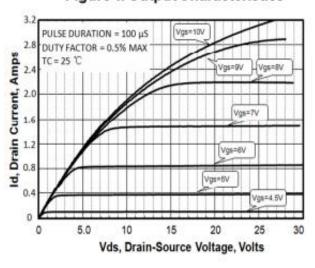
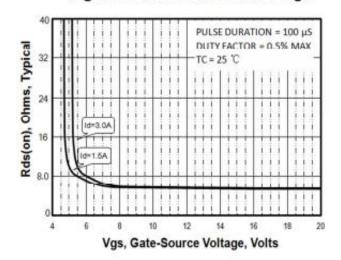


Figure 5. Rdson vs Gate Voltage

Tc, Case Temperature, 'C





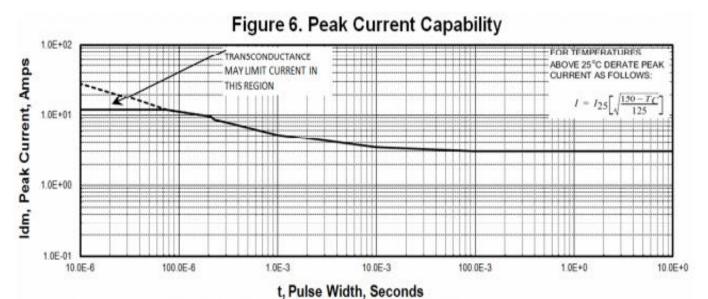
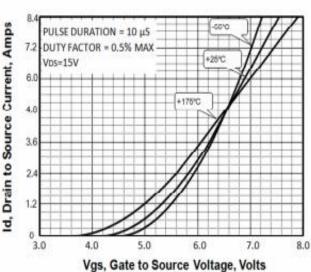


Figure 7. Transfer Characteristics



Vgs, Gate to Source Voltage, Volts

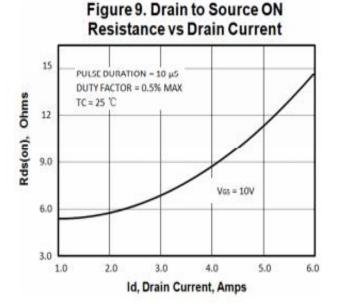


Figure 8. Unclamped Inductive Switching Capability

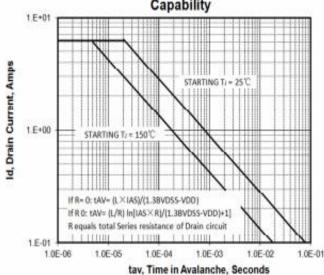


Figure 10. Rdson vs Junction

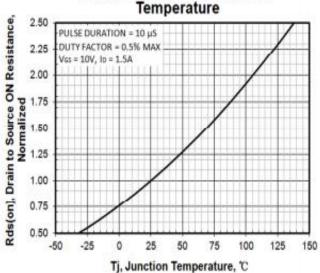




Figure 11.Breakdown Voltage vs
Temperature

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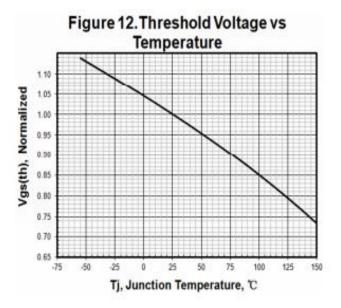
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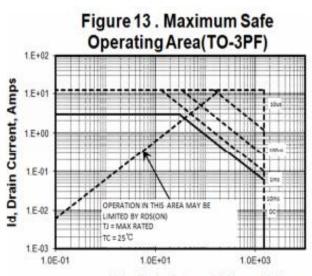
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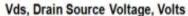
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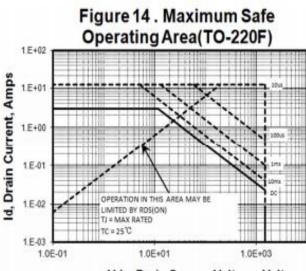
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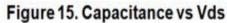








Vds, Drain Source Voltage, Volts



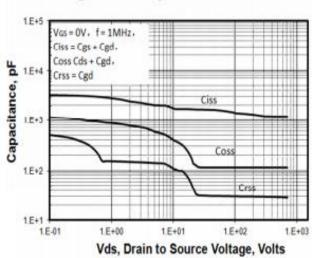
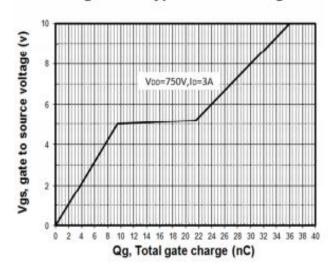


Figure 16 . Typical Gate Charge





## **Test Circuits and Waveforms**

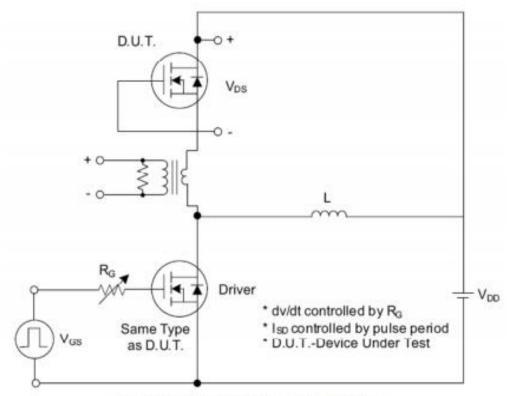


Fig. 1.1 Peak Diode Recovery dv/dt Test Circuit

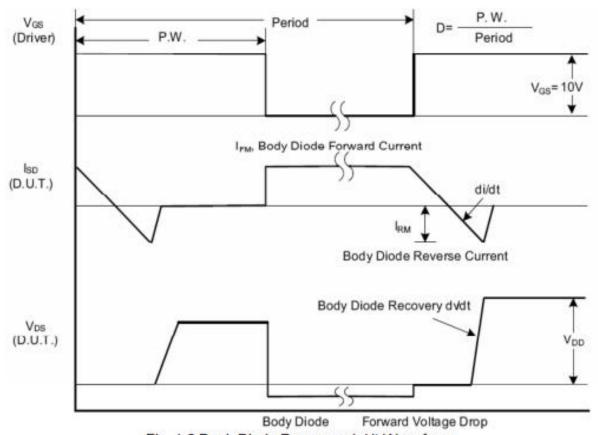


Fig. 1.2 Peak Diode Recovery dv/dt Waveforms

## **Test Circuits and Waveforms**

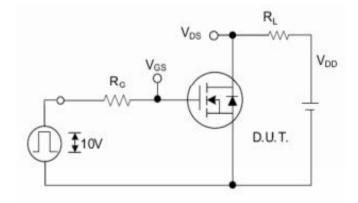


Fig. 2.1 Switching Test Circuit

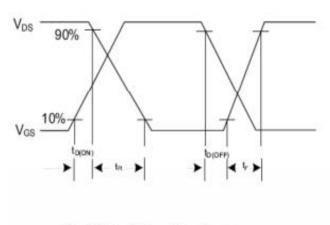


Fig. 2.2 Switching Waveforms

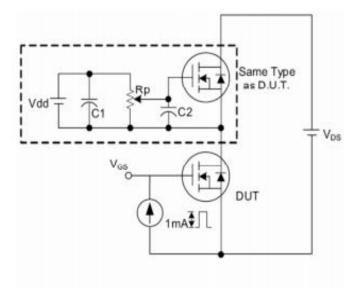


Fig. 3. 1 Gate Charge Test Circuit

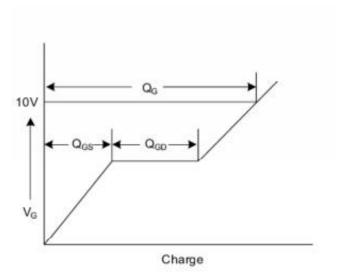


Fig. 3.2 Gate Charge Waveform

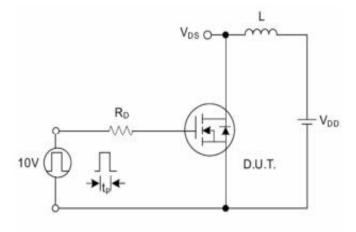


Fig. 4.1 Unclamped Inductive Switching Test Circuit

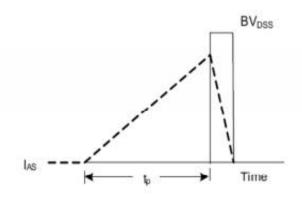
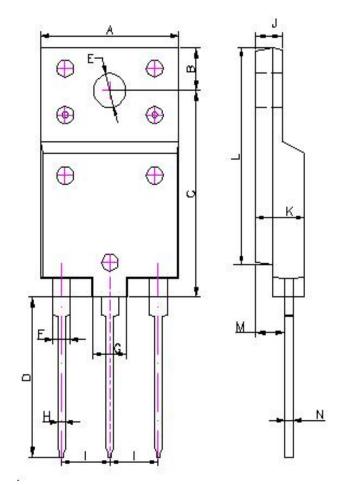


Fig. 4.2 Unclamped Inductive Switching Waveforms



# Package outline drawing(TO-3PF Unit: mm)



cyamor c	MILLIMETERS				
SYMBOLS -	MIN	MAX			
A	15. 30	15. 70			
В	4.30	4. 70			
С	21.80	22. 20			
D	16.70	17. 30			
Е	3. 45	3. 75			
F	1.85	2. 15			
G	3.85	4. 15			
Н	0.75	0.95			
I	5. 35	5. 55			
J	2.80	3. 20			
K	5. 30	5. 70			
L	22.80	23. 20			
M	3. 25	3. 55			
N	0.80	1.00			
P	14.4	15.00			



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