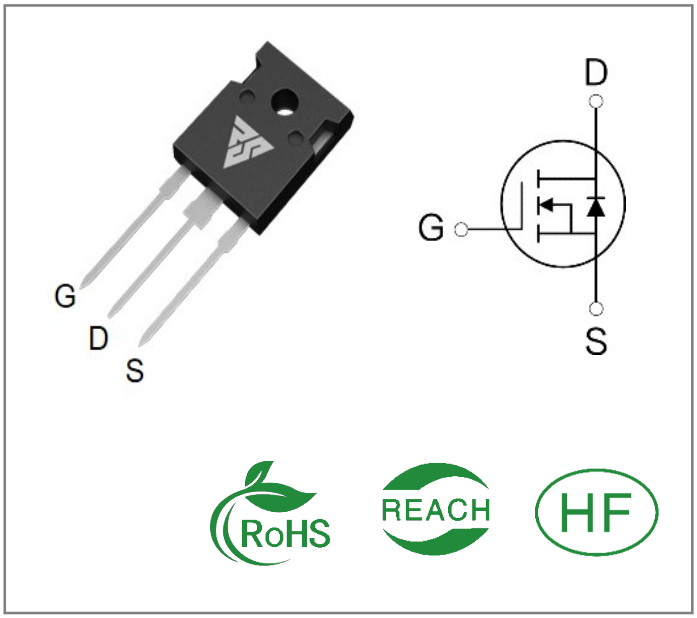
VDS	RDS(on)	ID@25℃
1200V	140mΩ	17A



Applications:

- Solar Inverters
- Switch Mode Power Supplies
- High Voltage DC/DC Converters
- EV Charging
- Motor Drives

Features:

- High Blocking Voltage with Low On-Resistance
- High Speed Switching with Low Capacitances
- Easy to Parallel and Simple to Drive
- Avalanche Ruggedness

Benefits:

- Higher System Efficiency
- Reduced Cooling Requirements
- Increased Power Density
- Increased System Switching Frequency

Ordering Information

Part Number	Package	Marking	Packing	Qty.
RSM120160W	TO-247-3	RSM120160W	Tube	30 PCS

Maximum Ratings (TJ= 25℃ unless otherwise specified)

Symbol	Parameter	Value	Unit	Test Conditions	Note
VDSmax	Drain - Source Voltage	1200	V	VGS=0V,ID =100μA	
VGSmax	Gate - Source Voltage	-8/+22	V	Absolute maximum values	
VGSop	Gate - Source Voltage	-4/+18	V	Recommended operational values	
ID	Continuous Drain Current	17 12	A	VGS=18V, TC =25℃ VGS=18V, TC =100℃	
ID(pulse)	Pulsed Drain Current	39	A	Pulse width tp limited by TJmax	
PD	Power Dissipation	83	W	TC =25℃, TJ =175℃	
TL	Solder Temperature	260	℃		
TJ, Tstg	Operating Junction and StorageTemperature	-55 to + 175	℃		

Electrical Characteristics (TJ= 25℃ unless otherwise specified)

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions	Note
V(BR)DSS	Drain-Source Breakdown Voltage	1200			V	VGS=0V,ID =100μA	
VGS(th)	Gate Threshold Voltage	1.9	2.6	4.0	V	VGS= VDS, IDS=2.5mA, TC =25℃	
			1.8		V	VGS= VDS, IDS=2.5mA, TC =175℃	
IDSS	Zero Gate Voltage Drain Current		1	100	μA	VDS= 1200V, VGS=0V	
IGSS	Gate-Source Leakage Current		10	250	nA	VGS=22V, VDS= 0V	
RDS(on)	Drain-Source on-state Resistance		140	185	mΩ	VGS=18V, ID =8.5A, TC =25℃	
			248	300	mΩ	VGS=18V, ID =8.5A, TC =175℃	
Ciss	Input Capacitance		612		pF	VGS=0V, VDS=1000 V, f=1MHz，VAC=25 mV	
Coss	Output Capacitance		34.5				
Crss	Reverse Transfer Capacitance		8.77				
EON	Turn-On Switching Energy		305		uJ	VDS =800V, VGS=-4/18V,ID = 8.5A, RG(ext) = 2.5Ω, L= 100μH	
EOFF	Turn-Off Energy		48				
td(on)	Turn-On Delay Time		7		ns	VDS =800V, VGS =-4/18 V ID = 8.5A, RG(ext) =2. 5 Ω , RL =20Ω	
tr	Rise Time		30				
td(off)	Turn-Off Delay Time		16				
tf	Fall Time		22				
RG(int)	Internal Gate Resistance		5		Ω	f=1 MHz, VAC=25mV	
Qgs	Gate to Source Charge		7.8		nC	VDS=800V, VGS=-4/18V ID =8.5A	
Qgd	Gate to Drain Charge		12.1				
Qg	Total Gate Charge		42.5				

Reverse Diode Characteristics (TJ= 25℃ unless otherwise specified)

Symbol	Parameter	Typ.	Max	Unit	Test Conditions	Note
VSD	Diode Forward Voltage	4.2		V	VGS=-4V, ISD =4.2A, TJ = 25℃	
		3.9		V	VGS=-4V, ISD=4.2 A, TJ= 175℃	
IS	Continuous Diode Forward Current		17	A	TC= 25℃	
trr	Reverse Recovery time	20		ns	ISD= 8.5 A, VR = 800V	
Qrr	Reverse Recovery Charge	29		nC		
Irrm	Peak Reverse Recovery Current	2.5		A		

Thermal Characteristics (TJ= 25℃ unless otherwise specified)

Symbol	Parameter	Typ.	Unit	Test Conditions	Note
RθJC	Thermal Resistance from Junction to Case	1.75	℃/W		
RθJA	Thermal Resistance From Junction to Ambient	40			

Typical Feature Curve

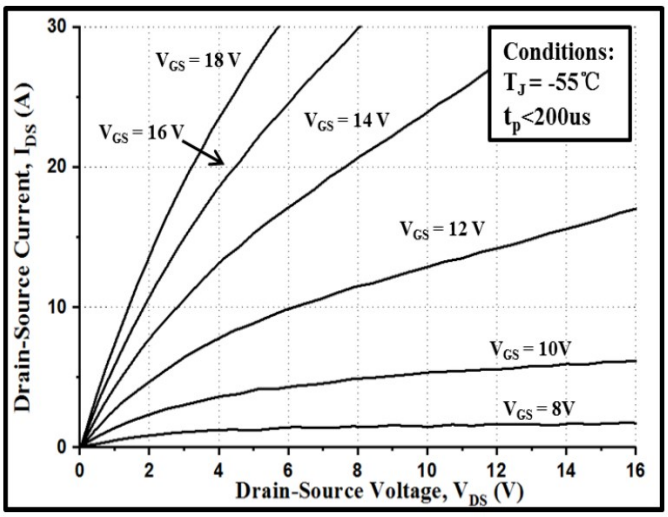


Figure 1. Output Characteristics T_J = -55℃

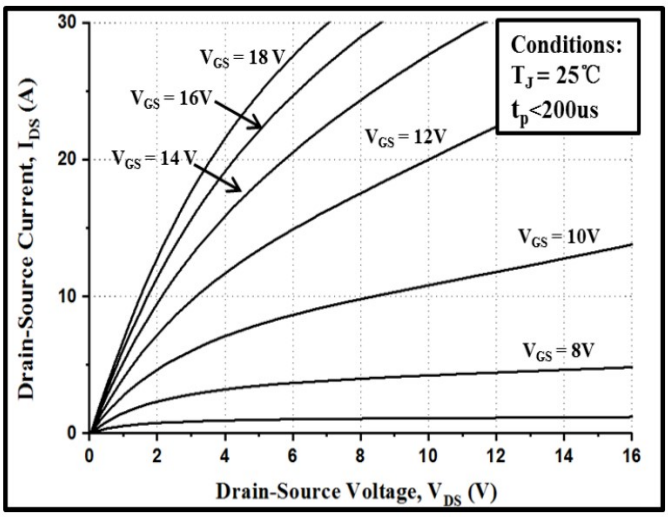


Figure 2. Output Characteristics T_J = 25℃

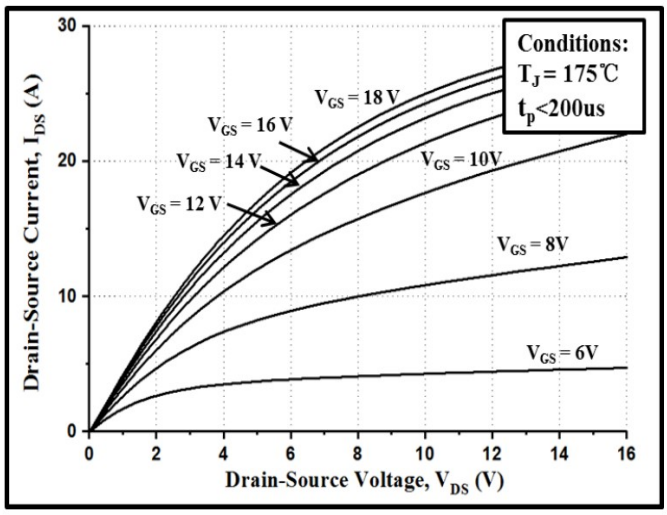


Figure 3. Output Characteristics T_J = 175℃

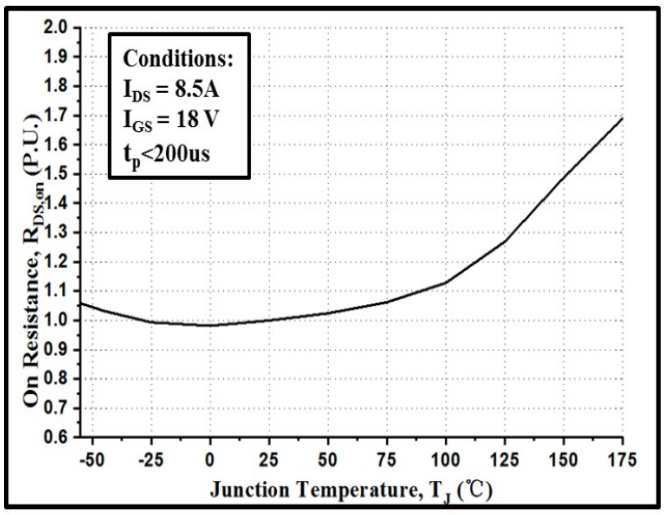


Figure 4. Normalized On-Resistance vs. Temperature

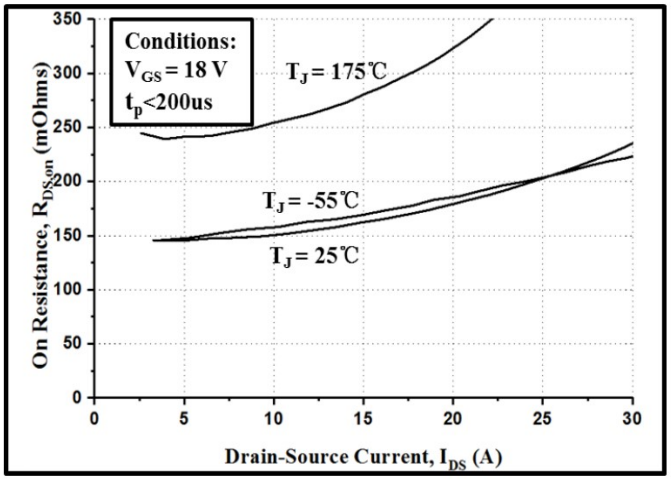


Figure 5. On-Resistance vs. Drain Current
For Various Temperatures

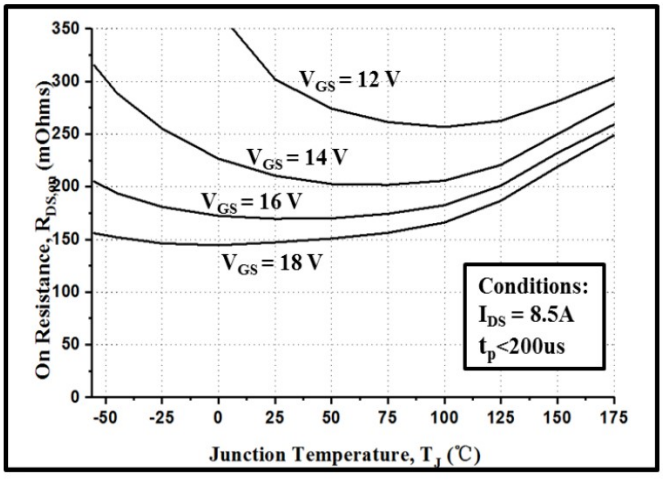


Figure 6. On-Resistance vs. Temperature
For Various Gate Voltage

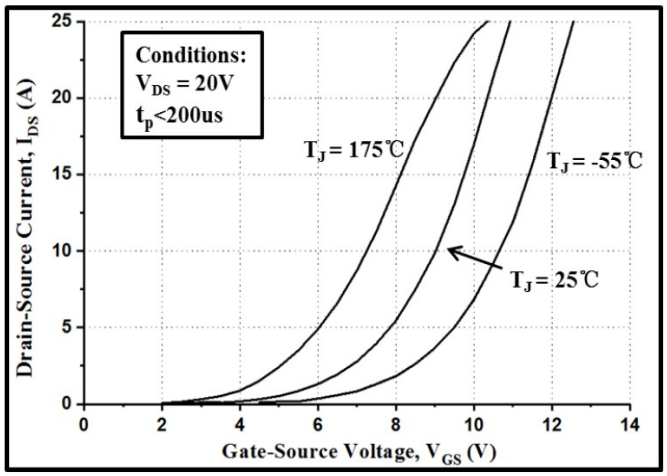


Figure 7. Transfer Characteristic for Various Junction Temperatures

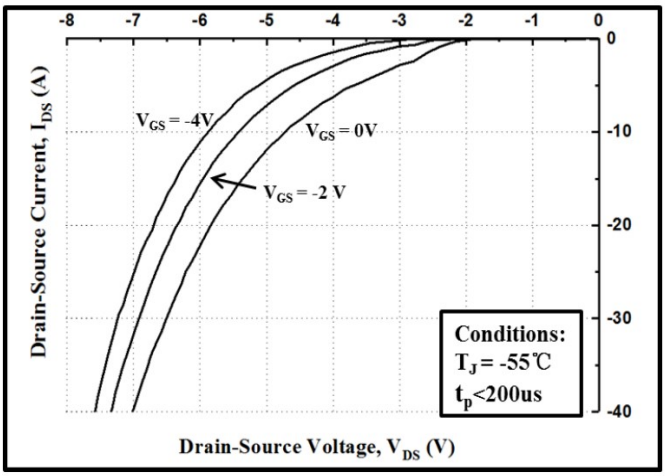


Figure 8. Body Diode Characteristic at -55℃

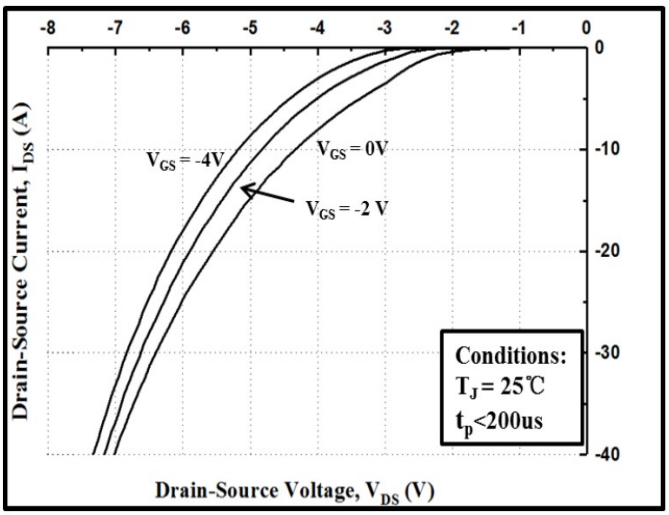


Figure 9. Body Diode Characteristic at 25℃

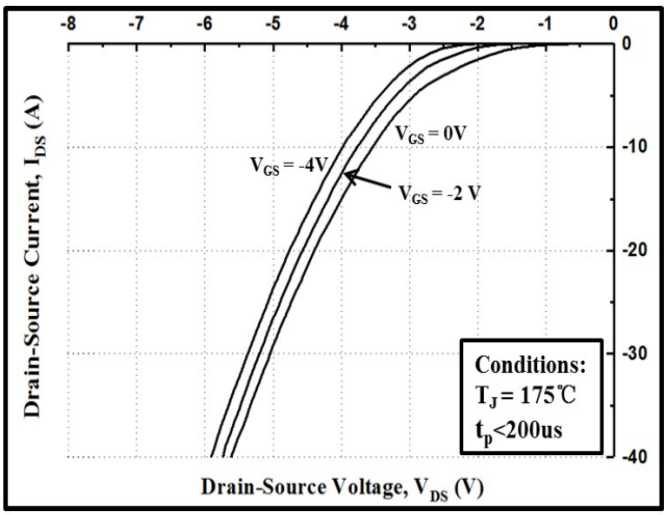


Figure 10. Body Diode Characteristic at 175℃

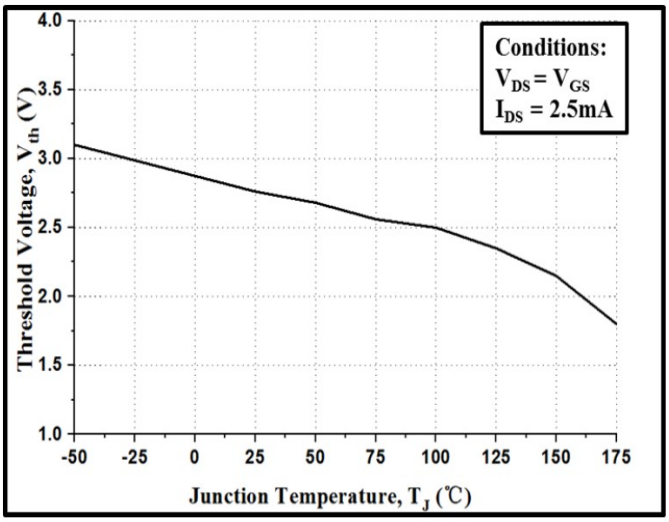


Figure 11. Threshold Voltage vs. Temperature

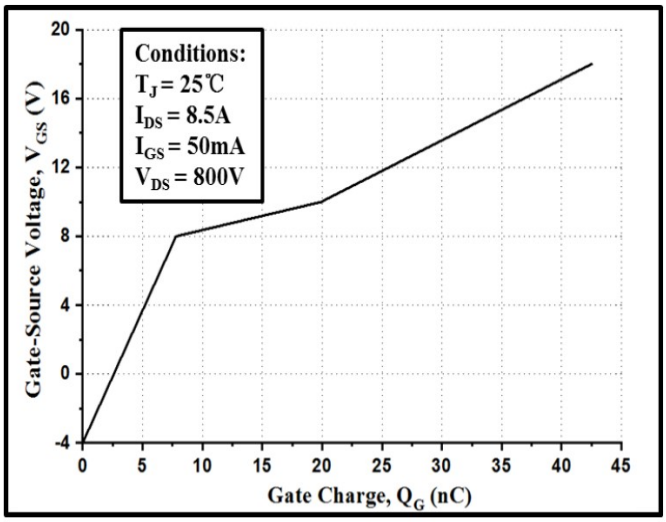


Figure 12. Gate Charge Characteristics

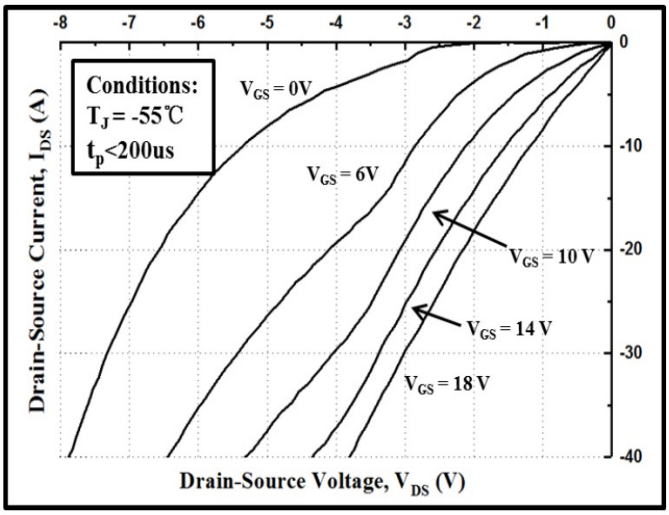


Figure 13. 3rd Quadrant Characteristic at -55℃

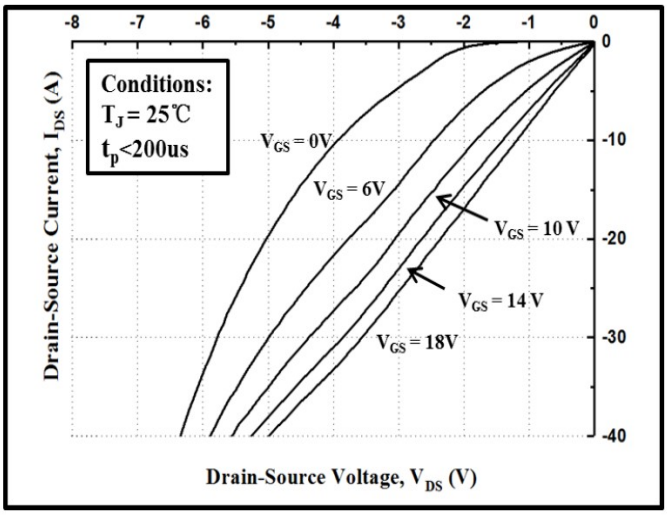


Figure 14. 3rd Quadrant Characteristic at 25℃

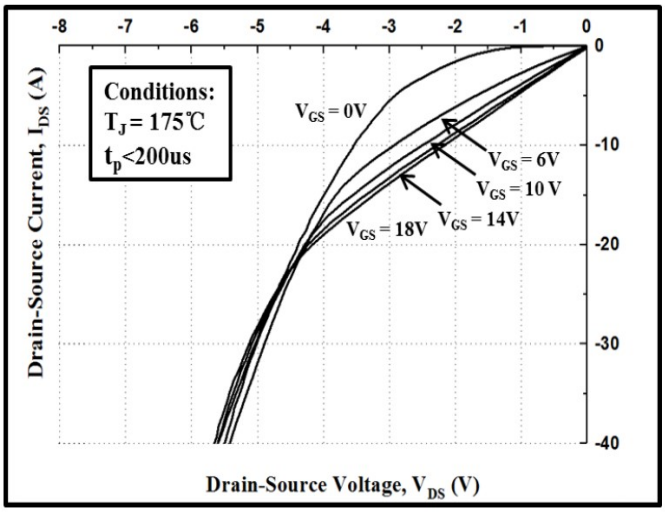


Figure 15. 3rd Quadrant Characteristic at 175 ℃

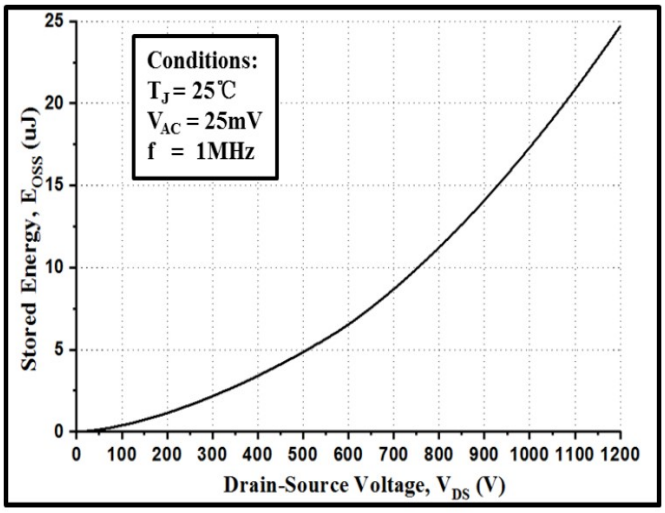


Figure16. Output Capacitor Stored Energy

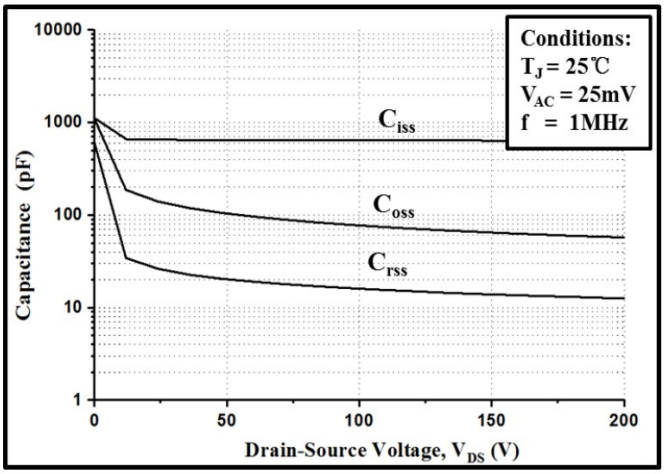


Figure 17. Capacitances vs. Drain-Source Voltage (0 - 200V)

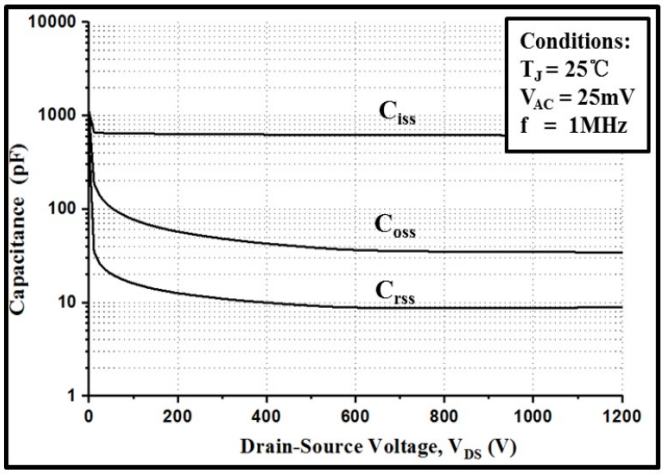


Figure 18. Capacitances vs. Drain-Source Voltage (0 - 1200V)

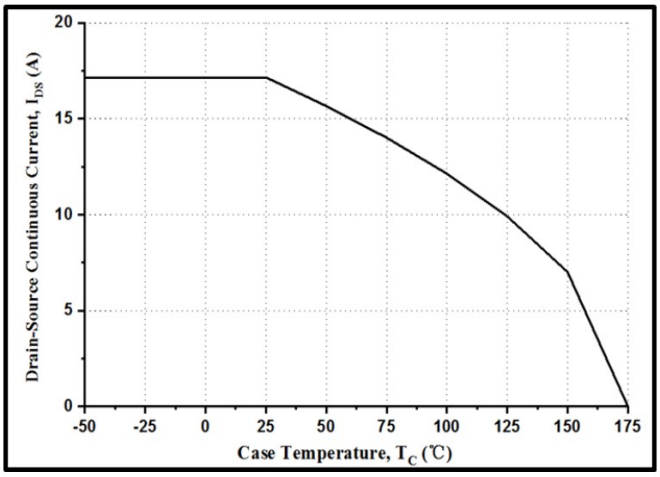


Figure 19. Continuous Drain Current Derating vs. Case Temperature

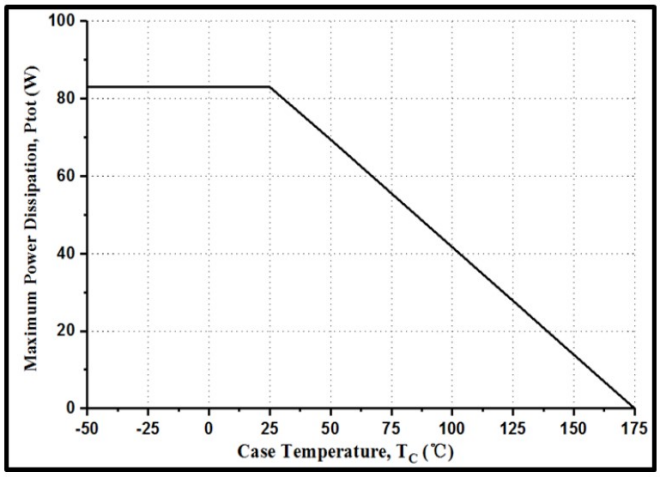


Figure 20. Maximum Power Dissipation Derating vs. Case Temperature

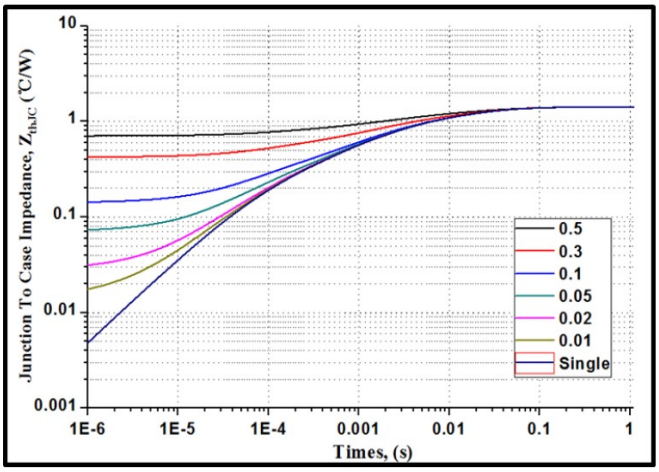


Figure 21. Transient Thermal Impedance (Junction - Case)

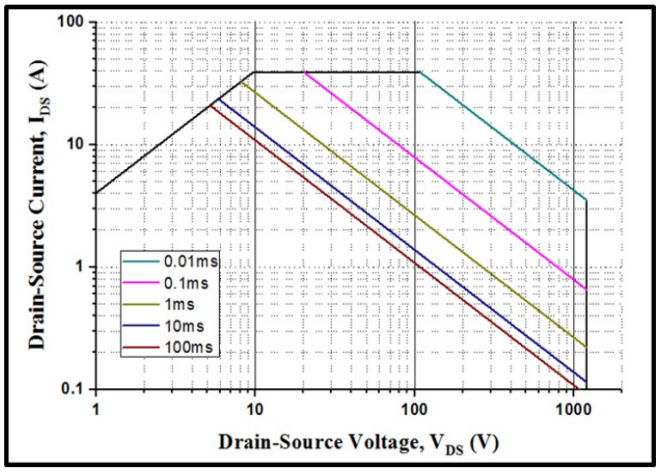


Figure22. Safe Operating Area

Test Circuit Schematic

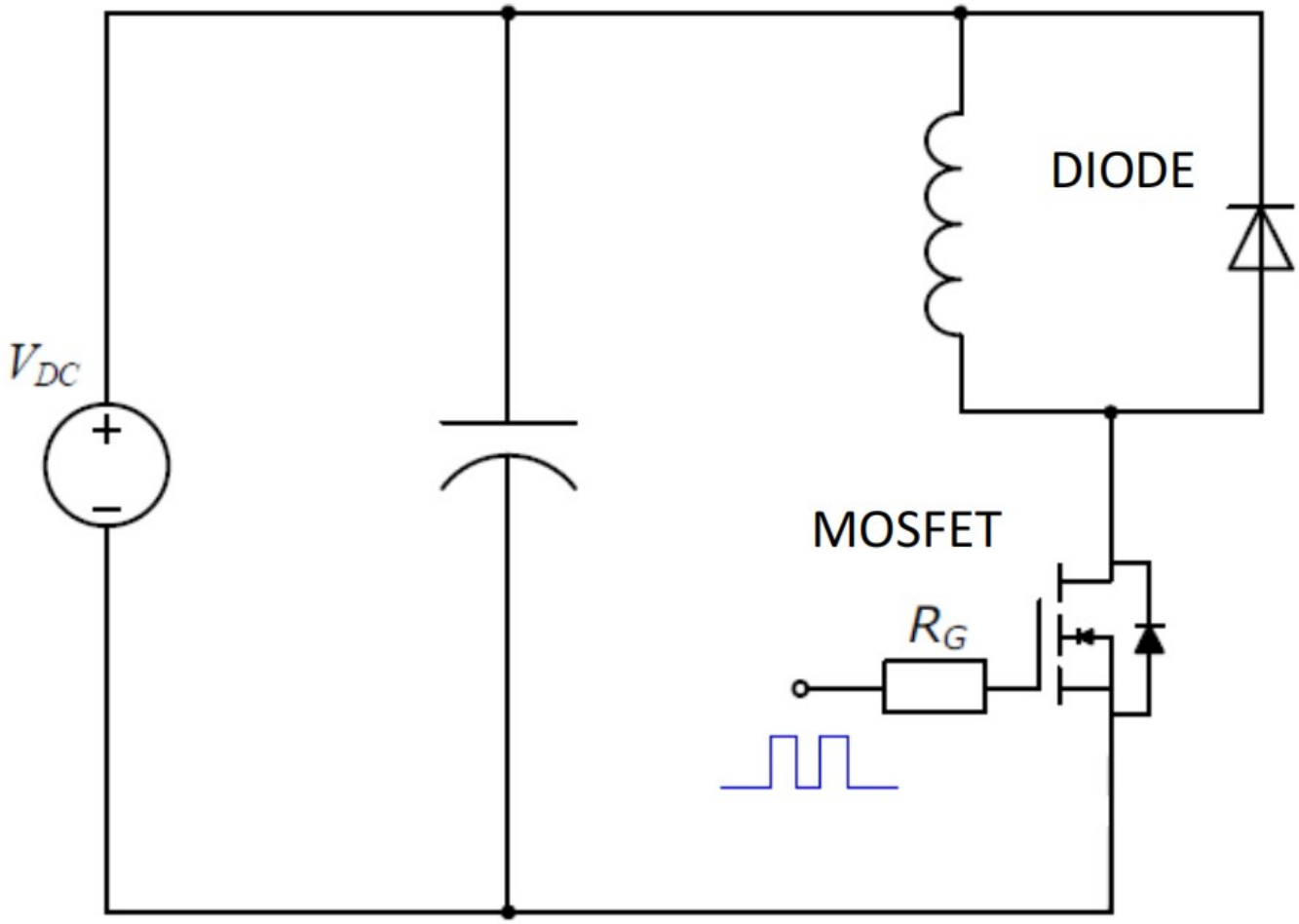
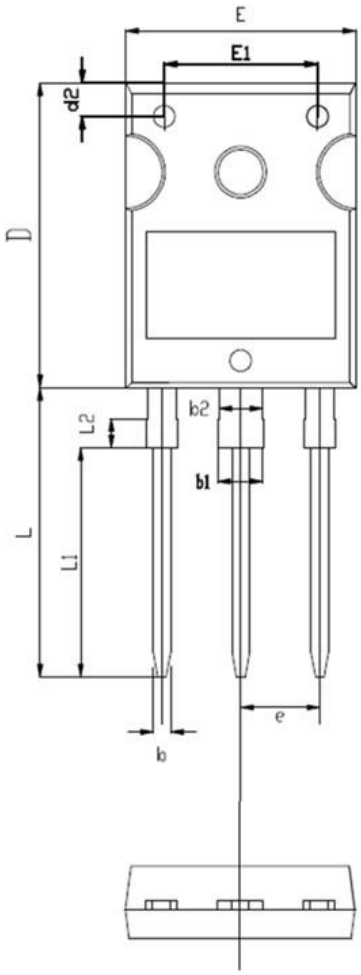
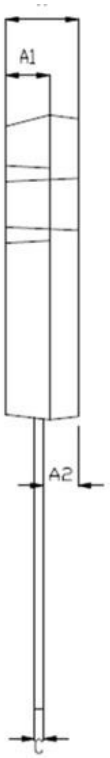
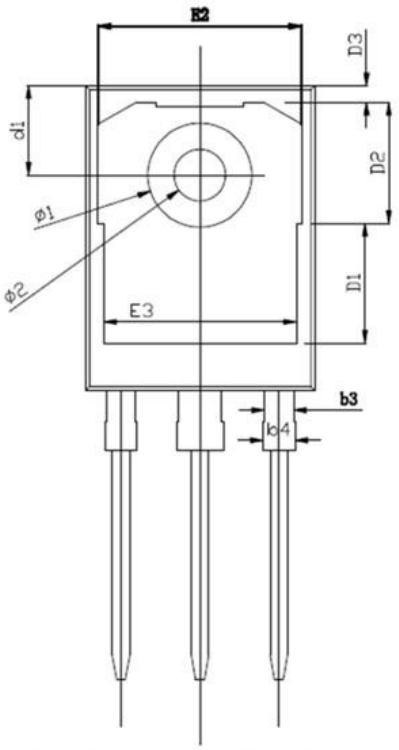


Figure 23. Clamped Inductive Switching Waveform Test Circuit

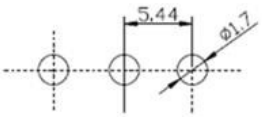
Package outline drawing(TO-247-3 Unit: mm)







RECOMMENDED LAND PATTERN



UNIT: mm

	MIN	NOM	MAX
A	4.80	5.00	5.20
A1	2.80	3.00	3.20
A2	2.26	2.41	2.56
b	1.10	1.20	1.30
b1	2.90	–	3.20
b2	2.90	3.00	3.10
b3	1.90	2.00	2.10
b4	2.00	–	2.20
c	0.50	0.60	0.70
D	20.80	21.00	21.20
D1		8.23	
D2		8.32	
D3		1.17	
d1	6.00	6.15	6.30
d2	2.20	2.30	2.40
E	15.60	15.80	16.00
E1		10.50	
E2		14.02	
E3		13.50	
e	5.34	5.44	5.54
L	19.72	19.92	20.12
L1		15.79	
L2		1.98	
ø1	7.10	7.19	7.30
ø2	3.50	3.60	3.70

Disclaimers:

Reasunos Semiconductor Technology Co.Ltd (Reasunos) reserves the right to make changes without notice in order to improve reliability,function or design and to discontinue any product or service without notice .Customers should obtain the latest relevant information before orders and should verify that such information in current and complete.All products are sold subject to Reasunos's terms and conditions supplied at the time of orderacknowledgement.

Reasunos Semiconductor Technology Co.Ltd warrants performance of its hardware products to the speciffications at the time of sale.Testing,reliability and quality control are used to the extene Reasunos deems necessary to support this warrantee. Except where agreed upon by contr- actual agreement,testing of all parameters of each product is not necessarily performed.

Reasunos Semiconductor Technology Co.Ltd does not assume any liability arising from the use of any product or circuit designs described herein.Customers are responsible for their products and applications using Reasunos's components.To minimize risk,customers must provide adequate design and operating safeguards.

Reasunos Semiconductor Technology Co.Ltd does not warrant or convey any license eith- er expressed or implied under its patent rights,nor the rights of others.Reproduction of inform- ation in Reasunos's data sheeets or data books is permissible only if reproduction is without modification oralteration.Reproduction of this information with any alteration is an unfair and deceptive business practice. Reasunos Semiconductor Technology Co.Ltd is not responsi- ble or liable for such altered documentation.

Resale of Reasunos's products with statements different from or beyond the parameters stated by Reasunos Semiconductor Technology Co.Ltd for that product or service voids all exp- ress or implied warrantees for the associated Reasunos's product or service and is unfair and deceptive business practice. Reasunos Semiconductor Technology Co.Ltd is not responsi- ble or liable for such statements.

Life Support Policy:

Reasunos Semiconductor Technology Co.Ltd's Products are not authorized for use as cri- tical components in life support devices or systems without the expressed written approval of Reasunos Semiconductor Technology Co.Ltd.
As used herein:
1. Life support devices or systems are devices or systems which: a.are intended for surgical implant into the human body, b.support or sustain life,
c.whose failuer to when properly used in accordance with instructions for used provided in the laeling,can be reasonably expected to result in significant injury to the user.

2.A critical component is any component of a life support device or system whose failure to system whose failure to perform can be reasonably expected to cause the failure of the life support device or system,or to affect its safety or effectiveness.